Sheet 1 of

Substitute Form PTO-1449 (Modified)

(37 CFR §1.98(b))

U.S. Department of Commerce Patent and Trademark Office Attorney's Docket No. 12732-171001

Applicant

Application No. 10/694,986

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Information Disclosure Statement by Applicant

(Use several sheets if necessary)

Masaharu Nagai et al.

Filing Date October 29, 2003 Group Art Unit 1756

			U.S. Pate	ent Documents			
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Filing Date Subclass If Appropriat	
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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Irans	lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL							
	AM							
-	AN							_
	AO				<u> </u>			
	AP							L

Other Documents (include Author, Title, Date, and Place of Publication)					
Examiner Initial	Desig. ID	Document			
DCD	AQ	Hatano et al., "A Novel Self-aligned Gate-overlapped LDD Poly-Si TFT with High Reliability and Performance", IEDM 97; Technical Digest of International Electron Devices Meeting, December 7, 1997, pp. 523-526.			
DCD	AR	Final Office Action dated December 06, 2005 for U.S. Serial No. 10/405,908 (Your Ref.: 12732-150001, Our Ref.: US6362)			
- <u>-</u>	AS				
	AT				

Examiner Signature

Date Considered

/Daborah Chacko-Davis/ (05/09/2006)

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)